

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	4087477
<b>Application Number:</b>	09328910
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	7455
<b>Title of Invention:</b>	LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME AND METHOD OF AND PROCESS FOR FABRICATING THE TWO
<b>First Named Inventor/Applicant Name:</b>	YUJIRO KAJIHARA
<b>Customer Number:</b>	20457
<b>Filer:</b>	Paul J. Skwierawski/Stacey Keaton
<b>Filer Authorized By:</b>	Paul J. Skwierawski
<b>Attorney Docket Number:</b>	1374.32049R00
<b>Receipt Date:</b>	09-OCT-2008
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<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$ 180
RAM confirmation Number	112
Deposit Account	
Authorized User	

### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement Letter	32049R00IDS.pdf	59983 36e94b69ca3c30ba7f100cbee6f354c5f865	no	2
<b>Warnings:</b>					
<b>Information:</b>					
2	Information Disclosure Statement (IDS) Filed (SB/08)	32049R00SB08.pdf	130184 c3ac7d4462d1440a1d020a6c1b6c78c7d7738e	no	1
<b>Warnings:</b>					
<b>Information:</b>					
This is not an USPTO supplied IDS fillable form					
3	Information Disclosure Statement Letter	32049OA.pdf	333994 2f18407138edf57b20c8a495fa2275ab6d0f1116	no	5
<b>Warnings:</b>					
<b>Information:</b>					
4	Foreign Reference	32049REF.pdf	268195 713c5c61f471aa3b72c53250c0c273b70e2ab	no	5
<b>Warnings:</b>					
<b>Information:</b>					
5	Fee Worksheet (PTO-06)	fee-info.pdf	30783 50115203ba70af16c2990a88405c70c14d8d17	no	2
<b>Warnings:</b>					
<b>Information:</b>					
Total Files Size (in bytes):			823139		
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